

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of** )  
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**HANZAWA et al.** )  
 )  
**Application Number: to be assigned** )  
 )  
**Filed: Concurrently Herewith** )  
 )  
**For: SEMICONDUCTOR MEMORY DEVICE** )  
**EQUIPPED WITH DUMMY CELLS** )  
 )  
**Attorney Docket No. NITT.0155** )

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**INFORMATION DISCLOSURE STATEMENT**

**Sir:**

The above-referenced application is a continuation of U.S. Serial No. 10/315,938 filed on December 11, 2002, which is a continuation of U.S. Serial No. 09/942,558 filed on August 31, 2001 (which issued on January 28, 2003, as U.S. Patent No. 6,512,714 B). It includes the same disclosure as U.S. patent application Serial No. 10/315,938.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application. 37 CFR Section 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

This Information Disclosure Statement is submitted with the above-captioned U.S. continuation application. Accordingly, no fee is due or payable at this time.

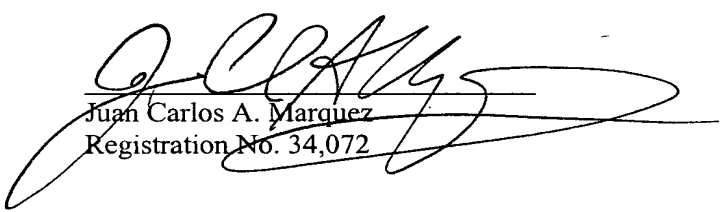
The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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**September 12, 2003**

